# 1 sirius\_analog

## 1.1 Overview

Sirius abb\_top contains

1. 4 channels of pipelined I/Q ADCs, which is 12 bit 100MS/s, fully differential input.
2. 2 channels of current I/Q DACs, which is 12 bit 200MS/s, fully differential output
3. 2 10bit 9MS/s SAR ADCs with 3 channel single end input
4. 1 10bit 9MS/s SAR ADCs with 8 channel single end input
5. Embedded bias voltage/current bias circuit
6. Embedded crystal driver
7. Temperature sensor
8. PVT sensor
9. ADDAPLL
10. DSPPLL0/1/2
11. SD card PLL
12. Audio PLL
13. Pixel PLL
14. DDRPLL
15. A7 PLL
16. CEVA PLL

**1.3 Architecture**

